fX3 Dual-speed 8-port Modules

SFP28 Test Modules 25/10G

The Spirent fX3 Ethernet multi-speed test modules combine Spirent's industry-leading Layer 2–3 traffic generation and analysis with powerful network emulation and application layer protocols for emulating a wide range of device types, users and protocols. These modules deliver the highest performance per dollar for Layer 2–3 testing. Reduced power consumption and dual-speed support results in lower CAPEX and OPEX. These modules are ideal for functional and performance testing of data center and service provider network infrastructure where extreme protocol performance is not required.

These modules are designed with eight native SFP28 ports to utilize the latest transceivers and interconnects. The dual-speed SFP28 interfaces are combined with Spirent's flexible FPGA logic to allow mode-switching of the fX3 packet generation and analysis engine to operate at 25 and 10G speeds. The fX3 module family is available in several speed combinations to match your test needs and budget.

Applications

- Service Provider Core and Edge Routers—Verify scale, reliability, and performance of Layer 2 & 3 services including IP data and video delivered via unicast routing, multicast routing, switching, Multicast VPN, EVPN and MPLS VPN technologies.
- Data Center Top of Rack, NIC, Spine and Core Switches—Benchmark capacity of high-density and capacity fabrics using IETF RFC 2544, RFC 2889 and RFC 3918 methodologies with easy test setup using dynamically bound traffic and automated wizards.
- Carrier Ethernet—Verify scale, reliability, performance of Ethernet services delivered via Ethernet OAM (CFM IEEE 802.1ag and Y.1731), MPLS-TP, VPLS, PWE3 Pseudowires, bridged Ethernet, packet transport protocols or combinations of these technologies.



Ospirent

Spirent's fX3 family of products are the industry's first native SFP28 form- factor test modules for multispeed High Speed Ethernet (HSE) testing:

- Supporting both 25G and 10G port speeds to test nextgeneration server and storage solutions.
- 25G and 10G copper and fiber media support
- Designed for enterprise and data center switch and router testing

Features & Benefits

- Dual speed versions provide flexibility for validating multispeed switches, NIC's and line cards. 25G-only versions also available to fit your budget needs
- Enable and disable Clause 74 BASE-R FEC, Clause 91 RS-FEC, and Clause 108 RS-FEC
- Auto Negotiation and Link Training for 25G
- SFP28 connector form-factor supports the latest 25/10G based copper and optical media
- Low total cost of ownership compared to other test modules in its class:
 - Excellent price-performance ratio that delivers faster time-to market by combining leading-edge technical innovation with Spirent's extensive testing experience
 - Intelligent power control to shut down unused test modules and allows faster boot time to bring capacity back on-line quickly
 - More total throughput than the competition for a given power footprint
 - Enhanced chassis software license value—Two to four times the device or end-user emulation per chassis with no increase in software costs
 - Topology emulation lowers Capex by eliminating the need for multiple DUTs in multiprotocol tests
 - Intelligent results gets answers in a fraction of the test time required by competitive products
 - Faster boot and firmware upgrade times mean less downtime in continuous running 24x7 regression test beds

- Spirent TestCenter's industry-leading Layer 2–3 feature set:
 - Stress ASIC and backplane designs with live traffic changes. The number of emulated devices, the traffic they emanate and the rate at which they send it can all be changed "on the fly" making for more realistic tests and faster troubleshooting
 - Best-in-industry for measuring ultra-low submicrosecond latencies with 2.5ns precision and resolution. Latency accuracy up to 10 times better than the competition
 - 19 different scheduling algorithms available for finding the right traffic to emulate the real world or tax the device's ability to handle any traffic pattern—from micro-bursts to carefully timed sequences of "killer" frames
- fX3 modules support Spirent TestCenter's deep analysis system:
 - Port counts, rates, errors and protocol summaries provide a high-level view for quick drilldown to specific issues
 - Broadest set of per stream metrics with simultaneous control and data plane results allows most tests to be run in a single pass
 - Real-time traffic filters allow analysis down to specific fields. Multiple metrics can be simultaneously collected and instantly analyzed
 - Dynamic views feature multi-metric extraction, sorting and operation in real-time or post-test
 - Full packet capture enables timing, sequencing and content analysis for individual packets
 - Powerful filters ensure the capture buffer is filled with relevant data



ipeed	Maximum ports	Maximum ports per					
•		Maximum ports per	fX3 Dual-speed 8-port Modules				
F /10C	per slot	SPT-N12U chassis	Maximum ports per SPT-N4U chassis				
5/10G	8/8	96/96	16/16				
G Only	8	96	16				
8-port SFP28							
Support varies by module speed mode • 25G: 802.3by 25GBASE-CR, 25GBASE-CRS, 25GBASE-SR • 10G: 10GBASE-SR, 10G Copper DAC • Auto-Negotiation and Link Training for 25G • Clause 74 BASE-R FEC, Clause 91 RS-FEC, and Clause 108 RS-FEC • IEEE 25GBASE CR CL74, CL108, CR-S CL74, SR FEC CL108 • 25/50G Consortium 25GBase-R FEC CL74, 25/50G Consortium 25GBase RS-FEC CL91							
 Stratum-3 rated oscillator is the default time source Frame time stamp resolution of 2.5ns GPS and CDMA-based external time sources are supported 							
 Ports in the same chassis are locked to the internal timing source. For separate systems: Timing chain synchronization with +/- 20ns accuracy Synchronized via GPS or CDMA network Using NTP or PTP packet-based approaches (requires supporting controller version) 							
Per-port reserve	ation						
TX/ 64K TX and	RX/128K for all speeds						
 Rate Based – individual stre Priority Based priorities. Pre 	key parameters determ eam blocks d – scheduling determin cise scheduling of CBR o	ined at the port level with di ed at the stream block level and bursty traffic for QoS tes	using user-assigned				
100% line rate fo	or frames of 64–16383 by	•	ment, decrement, randor				
checksum and • Over 40 real-	d CRC errors time measurements per						
	• •	ch 25/10G port or each SFP2	8 interface for 25G				
		ted trigger and filtering cont	trols				
Port-level histog	gram modes for latency,						
	Support varies k 25G: 802.3by 10G: 10GBASE Auto-Negotia Clause 74 BAS IEEE 25GBASE 25/50G Conse Stratum-3 rat Frame time st GPS and CDN IEEE 1588v2 a TIA/EIA-95B- Ports in the sam Timing chain Synchronized Using NTP or Per-port reserve TX/ 64K TX and 6 VFDs availa 8m route inse Port Based – Rate Based – individual stre Priority Based priorities. Pre Manual Mode 100% line rate for and IMIX modes Nearly 50 tran checksum and Over 40 real- and data inte +/- 100 PPM in 1 and 10G 512MB per 25 Capture softw Port-level histog length and sequ	Support varies by module speed mode 25G: 802.3by 25GBASE-CR, 25GBASE 10G: 10GBASE-SR, 10G Copper DAC Auto-Negotiation and Link Training for Clause 74 BASE-R FEC, Clause 91 RS-1 IEEE 25GBASE CR CL74, CL108, CR-S (25/50G Consortium 25GBase-R FEC (Stratum-3 rated oscillator is the defa Frame time stamp resolution of 2.5ns GPS and CDMA-based external time IEEE 1588v2 and NTP packet-based ex- TIA/EIA-95B-based external time sour Ports in the same chassis are locked to the Timing chain synchronization with +/- Synchronized via GPS or CDMA netw Using NTP or PTP packet-based apprent Per-port reservation TX/ 64K TX and RX/128K for all speeds 6 VFDs available for each 512 (25G/10) 8m route insertion table entries 4m in Port Based – traffic scheduling handl Rate Based – key parameters determindividual stream blocks Priority Based – scheduling determindividual stream blocks Priority Based – scheduling of CBR of Manual Mode – manual control of str 100% line rate for frames of 64-16383 by and IMIX modes Nearly 50 transmit stats per port repor- checksum and CRC errors Over 40 real-time measurements per and data integrity +/- 100 PPM in 1 PPM increments for eac and 10G 512MB per 25G/10G port Capture software includes sophistica Port-level histogram modes for latency, length and sequence difference check	Support varies by module speed mode 25G: 802.3by 25GBASE-CR, 25GBASE-CRS, 25GBASE-SR 10G: 10GBASE-SR, 10G Copper DAC Auto-Negotiation and Link Training for 25G Clause 74 BASE-R FEC, Clause 91 RS-FEC, and Clause 108 RS-FEC IEEE 25GBASE CR CL74, CL108, CR-S CL74, SR FEC CL108 25/50G Consortium 25GBase-R FEC CL74, 25/50G Consortium 25 Stratum-3 rated oscillator is the default time source Frame time stamp resolution of 2.5ns GPS and CDMA-based external time sources are supported IEEE 1588v2 and NTP packet-based external time sources are sup TIA/EIA-95B-based external time sources are supported Ports in the same chassis are locked to the internal timing source. For Timing chain synchronization with +/- 20ns accuracy Synchronized via GPS or CDMA network Using NTP or PTP packet-based approaches (requires supporting Per-port reservation TX/ 64K TX and RX/128K for all speeds 6 VFDs available for each 512 (25G/10G) stream templates 8 m route insertion table entries 4m in 25G/10G mode Port Based – traffic scheduling handled at the port level with di individual stream blocks Priority Based – scheduling determined at the stream block level priorities. Precise scheduling of CBR and bursty traffic for QoS test Manual Mode – manual control of stream sequence 100% line rate for frames of 64-16383 bytes controlled by fixed, increa and IMIX modes Nearly 50 transmit stats per port reported in real time. Includes L checksum and CRC errors Over 40 real-time measurements per stream including advanced and data integrity +/- 100 PPM in 1PPM increments for each 25/10G port or each SFP2 and 10G S12MB per 25G/10G port Capture software includes sophisticated trigger and filtering cont Port-level histogram modes for latency, jitter, inter-arrival time, france includes latency, jitter, inter-arrival time, france includes latency, jitter, inter-arrival time, france includes latency is the set on late				

About Spirent

Spirent Communications (LSE: SPT) is a global leader with deep expertise and decades of experience in testing, assurance, analytics and security, serving developers, service providers, and enterprise networks. We help bring clarity to increasingly complex technological and business challenges. Spirent's customers have made a promise to their customers to deliver superior performance. Spirent assures that those promises are fulfilled.

For more information visit: www.spirent.com

Technical Specifications (cont'd)

Spirent TestCenter Protocol Emulation

Spirent TestCenter protocols available as separately licensed packages. Below is a sample list of supported protocols. Contact Spirent for a full list of capabilities and packages.

Enterprise and data	• OpenFlow 1.3 / 1.0: OpenFlow switch and controller emulation testing
center switch protocol support	 Routing, multicast and bridging: All major IPv4 and IPv6 unicast and multicast routing protocols, IGMPv1/v2/v3, MLDv1/v2, LACP, STP, RSTP and MSTP
	• Data center: VXLAN, EVPN, DCBX, FCoE, FIP, 802.1Qbb
Service Provider Protocol support	 WAN SDN: PCE-P, BGP-LS (Link State), BGP Flow Spec and Segment Routing for ISIS, OSPF and BGP
	 NFV: Validate performance and scale for NFVI and VNFs including vSwitch, BGP v RouteReflector, vBNG, vCPE and vRouter
	 Routing and MPLS: All major IPv4 and IPv6 unicast and multicast routing protocols, RSVP-TE, LDP, VPLS-LDP, VPLS-BGP, BGP/MPLS- VPN, EVPN (RFC 7432), PBB EVPN, mVPN, NG MVPN, GTM, BFD, MPLS BFD, LSP Ping, TWAMP and PWE3 (RFC4447)
	 Access: ANCP, PPPoE, DHCP, L2TP, L2TPv3, IGMPv1/v2/v3, MLDv1/v2, DHCP-PD and PPPoEv6
	 Carrier Ethernet and bridging: LACP, STP, RSTP, MSTP, 802.1ag CFM, Y.1731, PBB, PBB-TE, Link OAM
	• Mobile Backhaul: MPLS-TP, 1588v2 and Synchronous Ethernet

Ordering Information		
Test Modules		
Description	Part Number	
Spirent FX3 25/10G SFP28 8-Port	FX3-25GD-S8	
Spirent FX3 25G SFP28 8-Port	FX3-25GO-S8	

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